

500.39919X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. YAMAGUCHI et al.  
Serial No.: 09/811,401  
Filed: March 20, 2001  
For: SEMICONDUCTOR DEVICE AND METHOD FOR  
MANUFACTURING THE SAME AND SEMICONDUCTOR  
DEVICE-MOUNTED STRUCTURE  
Group: 2815  
Examiner: L. Cruz

AMENDMENT

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

May 13, 2002

Sir:

In response to the Office Action dated December 13, 2001,  
the period of response for which extension is requested by the  
attached Petition for Extension of Time, please amend the  
above-identified application as follows:

09/18/2002 JMC/MILL 00000001 012135 09811401

01 FC:103 36.00 CH

In the Claims:

Please cancel claims ~~15~~ and ~~16~~ without prejudice.

Please amend claims 1-6, 8, 10, 12, and 13 as follows:

1. (Amended) A semiconductor device comprising:  
a semiconductor element having a circuit  
surface on which a plurality of circuit electrodes are

05/14/2002 ZJUHAR1 00000064 09811401

02 FC:103

144.00 OP



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#19/A  
Amend  
J. Macmillan  
9/17/02